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CALL FOR PAPERS

Authors are being invited to submit their original research papers previously unpublished, currently not under review by other conference or journal. Acceptance of paper will be based on quality, relevance and originality of the work. The participants are requested to submit extended abstract with electronic format in Microsoft Word 2010, confined to A4 size paper having a maximum 200 words typed with Times New Roman, 10 Font size, 1.5 line spacing, 2.54 cm margin on all the sides. Abstract/Paper should be submitted through EasyChair on or before 15th November 2020.

REGISTRATION DETAILS

It is mandatory to register at least one author after acceptance of the paper and to present it in the ICIEET-2021 oral/poster session to appear in the publication of proceeding/journal. Certificate will be issued only for registered author.

Delegate type	Indian (INR)	Foreign (\$)
o <i>n</i>		

- Industry
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Registration and accommodation fee can be paid through internet banking in the following bank account:

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IMPORTANT DATES:

 Abstract Submission 	15/11/2020
 Intimation of Acceptance 	30/11/2020
 Submission of Manuscript 	15/12/2020
 Payment of Registration fee 	30/12/2020
 Accommodation request 	15/02/2021

Publications:

All the accepted abstracts will be published in ICIEET-2021 abstract book (e-book).

Previous Publication:

https://iopscience.iop.org/issue/1757-899x/814/1 https://inep.sumdu.edu.ua/en/component/archive/ (Vol. 12 No.2)





International Conference

on **Innovations in Electrical & Electronics Technology (ICIEET-2021)**

March 05-06, 2021 http://icieet.sknscoe.ac.in

SINHGAD INSTITUTE PANDHARPUR

Organized by

Department of Electrical and Electronics & Telecommunication Engineering,

SKN SINHGAD COLLEGE OF ENGINEERING,

Korti, Pandharpur-413304, Maharashtra, India. (Affiliated to Punyashlok Ahilyadevi Holkar Solapur University, Solapur; Approved by DTE, Govt. of Maharashtra and AICTE)

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ABOUT INSTITUTE

SKN Sinhgad College of Engineering is housed in beautiful campus of 20 acres and developed under dynamic leadership of Hon'ble Prof. M. N. Navale. The Institute offers five B. Tech. four M. Tech. and two Ph. D degree courses in Engineering and Technology under Punyashlok Ahilyadevi Holkar Solapur University, Solapur. Since its establishment, the institute has made considerable progress in the field of Engineering education and research. As a result of this, the institute is accredited with 'A' grade by NAAC. The institute ensures the overall development of students through well-defined teaching learning process and skill based activities. Students will have an exposure for experiential learning through value based Education and on campus projects. The team of faculty members of SKNSCOE are committed to prepare their students as tomorrow's leaders to match the need of fast changing world under the guidance of Dr. Kailash J. Karande, Principal, SKNSCOE. All the laboratories of each departments have modern and state-of-art equipment's with all required hardware's and software's. The library is fully fledged with all required reference books, Journals, periodicals. The institute has well equipped hostel facilities, computer centre, hygienic mess and canteen, dispensary, ATM, Gymnasium, etc within the campus.

ABOUT DEPARTMENT

Departments of Electrical and Electronics & Telecommunication Engineering are engaged in fulfilling the much desired basic requirements of regular B. Tech., M. Tech & Ph.D. programs. The departments have various laboratories to cater for prescribed experiments at UG and PG levels. The departments have highly qualified and experienced faculty members who guide UG and PG students and motivates them towards research.

Furthermore, departments have been set up with the following research areas:

- Internet of Things
- RFID Antenna Design and fabrication
- Biomedical Image and Signal Processing

ABOUT PANDHARPUR

Pandharpur is unique holy place, known as south kashi of India and situated on the bank of Bhima river. The river Bhima is known as Chandrabhaga as it takes the shape of crescent moon near the town. Moreover, city is well known throughout India for God Vitthal. People arrive from all over the country to workship lord Vitthal. The warkaris start marching from their homes towards



VLSI and Embedded systems

Power Electronics & Drives

Renewable Energy

Power Quality

Pandharpur in groups called Dindi, about 10-15 lac warkaris visit the city as pilgrims on the occasions of Aashadhi Ekadashi and Kartiki Ekadashi. All the devotees are allowed to touch the feet of the God Vitthal. Vishnupad is a small square shaped platform on the bank of chandrabhaga. This temple houses the footprints of Lord Vitthal.

OBJECTIVE OF CONFERENCE

The conference ICIEET-2021 is focused on the theme 'Innovations in Electrical and Electronics Technology' which eventually aimed to bring together scientists, researchers, technologists and industrialists across the world to exchange their views and ideas for better possible scientific collaborations in the domain of interest.

CONFERENCE TOPICS

ICIEET-2021 is the premier forum for the presentation of new advances and research results in the field of Innovations in Electrical and Electronics Technology. The subject areas suitable for submission include artificial intelligence, renewable energy, power system, communication and their applications in technologies which have societal perspective, but not limited to the following fields:

Track 01: Innovations in Artificial Intelligence
Artificial Intelligence in EE
Automation with Electronic Controls
Biomedical Image and Signal Processing
Track 02: Internet of Things Sensor Automations
Internet of Things
Wireless Communication
Track 03: Renewable Energy System Design
Renewable Energy Sources and Electrical Vehicle
Advances in Control System
Power System Modeling and Simulation
Power Market and Power System Economics
Track 04: Power Transmission and Applications
Flexible AC Transmission System and HVDC System
Power Quality Issues, Power System Stability
Power Electronics and Drives
Track 05: RF System Modeling and Optimization
RF and Microwave Communication
VLSI and Embedded System Design
Communication Networks and Systems
Track 06: Inter-Material Science Design and Applications
Electro-ontical Engineering Materials

Electro-optical Engineering Materials Materials for Electrical and Electronics Devices

Journal Publication:

Registered/Accepted and presented papers will be published in Scopus/ SCI/SCIE journals after further reviews .

For more details visit: http://icieet.sknscoe.ac.in

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